

# Servo amplifier

## mcDSA-E66-Modul

Article number: 1505031



Picture similar

**Technical data**

<b>Supply voltages</b>		<b>Encoder</b>
Electronic supply voltage Ue* <sup>1</sup>	9..30 V	Type
Electronic current consumption@ Ue=24V* <sup>2</sup>	typ. 40 mA	Signals
Power supply voltage Up* <sup>3</sup>	9..60 V	Resolution
<b>Output current</b>		<b>Digital inputs</b>
Max. output current	15 A	Number - digital inputs
Continuous output current @ Up=24V* <sup>4</sup>	5 A	Number (0..30V tolerant)
Continuous output current @ Up=48V* <sup>4</sup>	4.3 A	Low voltage
<b>PWM</b>		High voltage
Output voltage	100% Up	Notice
PWM frequency	25, 32* <sup>5</sup> , 50 kHz	<b>Digital outputs</b>
<b>Mechanical</b>		Number
Size LxWxH	53 x 41 x 11 mm	Continuous output current
Weight	18 g	Load
<b>Environment</b>		Output voltage
Protection class	IP00	Signal type
Ambient temperature (operation)	-40..70 °C	Notice
Ambient temperature (storage)	-40..85 °C	<b>Analog inputs</b>
Rel. humidity (non-condensing)	5..90 %	Number
<b>CAN bus</b>		Signal type
Protocol	DS301	+/- 10 V, 12 Bit, single ended
Device profile	DS402	
Max. baudrate	1 Mbit/s	
CAN specification	2.0B	
Galvanically isolated	no	
<b>Auxiliary voltage</b>		
Output voltage	5 V	
Max. output current	0.2 A	

\*<sup>1</sup> No reverse polarity protection, the destruction limit is at overvoltage of >= 33V or short-term peak voltage of 37V < 1s\*<sup>2</sup> power amplifier switched off, 5V output (sensor supply) is free\*<sup>3</sup> No reverse polarity protection, the destruction limit is at overvoltage of >= 80V\*<sup>4</sup> connector cable with max. possible cable cross-section, PWM frequency 32 kHz, ambient temperature 40 °C (t >40 °C derating), RMS current: 5 A → 4.1 Aeff, 4.3 A → 3.5 Aeff  
no guarantee, since value is determined empirical, please consider the application notes to determine the continuous current\*<sup>5</sup> default value\*<sup>6</sup> Input voltage must not exceed Electronic supply voltage Ue

Additional technical data are available in mcManual.



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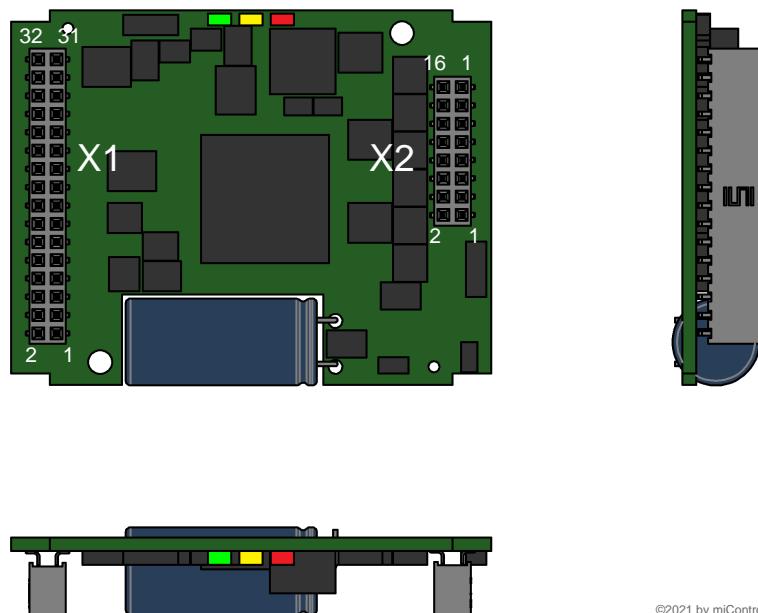
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## Scheme



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## Terminal assignment

X1	I/O's and CAN
1	res.
2	/Id7
3	+U5V
4	/Id6
5	res.
6	/Id5
7	res.
8	/Id4
9	res.
10	/Id3
11	res.
12	/Id2
13	res.
14	/Id1
15	CAN Lo
16	/Id0
17	CAN Hi
18	Erw2
19	Dout0
20	Erw1
21	Din2
22	SpiCLK
23	Din1
24	SpiMOSI
25	Din0
26	Spi/SS
27	Ain0
28	SpiMISO
29	Ain1
30	Din3/Dout1
31	GND
32	res.

X2	Motor
1	+Up
2	res.
3	+Up
4	res.
5	GND
6	GND
7	Ma
8	+Ue
9	Ma
10	+Ue
11	Mb
12	Mb
13	Mc
14	res.
15	Mc
16	res.